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- VAX architecture had an instruction to multiply polynomials!
- RISC philosophy (Cocke IBM, Patterson, Hennessy, 1980s)
- Reduced Instruction Set Computing
- Keep the instruction set small and simple, makes it easier to build fast hardware
- Let software do complicated operations by composing simpler ones

9

Introduction to Assembly: RISC-V Instruction Set Architecture

Which solution is efficient and secure? ... Computer Organization and Design MIPS Edition: The Hardware/Software Interface 5th Edition David A. Patterson, John L. Hennessy. 220 explanations. Introduction to the Theory of Computation 3rd Edition Michael Sipser. 389 explanations.

AWS Academy Cloud Architecting [2606] - Module 13 ...

Concurrent computing is a form of computing in which several

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computations are executed concurrently—during overlapping time periods—instead of sequentially—with one completing before the next starts.. This is a property of a system—whether a program, computer, or a network—where there is a separate execution point or "thread of control" for each process.

Concurrent computing - Wikipedia

Hennessy and Patterson foresee a move from general-purpose toward domain-specific architectures that run small compute-intensive kernels of larger systems for tasks such as object recognition or speech understanding. The key requirement is that the most expensive computations in the application domain have plenty of parallelism and locality.

There's plenty of room at the Top: What will drive ...

Also simply application or app. Computer software designed to perform a group of coordinated functions, tasks, or activities for the benefit of the user. Common examples of applications include word processors, spreadsheets, accounting applications, web browsers, media players, aeronautical flight simulators, console games, and photo editors. This contrasts with system software, which is ...

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